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**Reading Silicon: How to Reverse Engineer Integrated Circuits** Ken Shirriff has seen the insides of more integrated circuits than most people have seen bellybuttons. (This is an exaggeration.)

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**CMOS Digital VLSI Design** Prof. Sudeb Dasgupta Department of Electronics and Communication Engg IIT Roorkee.

**CMOS Digital VLSI Design**

**Electronics - VLSI Design**

**From Sand to Silicon: the Making of a Chip | Intel** This is how a microprocessor, the brain 'behind the magic' of your PC, is made. For more about process Intel employs in building ...

**IC Design & Manufacturing Process : Beginners Overview to VLSI** A simplified description of Complete process of IC Design and Manufacturing. <https://www.systemverilogacademy.com/> When ...

**VLSI Interview Questions and Answers 2019 Part-1 | VLSI Interview Questions | Wisdom Jobs** VLSI\_Interview\_Questions #VLSI #VLSI\_Interview\_Tips #Top\_10\_VLSI\_Interview\_Questions ...

**CMOS VLSI by Razavi**

**What is a CMOS? [NMOS, PMOS]** In this video I am going to talk about how a **CMOS** is formed.

**Reverse engineering a simple CMOS chip** Reverse engineering a National Semiconductor 54HC00 quad NAND gate ([https://project5474.org/index.php?title=54HC00\\_NS\\_8632](https://project5474.org/index.php?title=54HC00_NS_8632))

**LATCH UP IN CMOS - English Version** This video contain LATCH UP IN **CMOS** in English, for basic Electronics & **VLSI engineers**.as per my knowledge i shared the ...

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**Lecture - 39 Latch - up in CMOS** Lecture Series on **VLSI** Design by Dr.Nandita Dasgupta, Department of Electrical **Engineering**, IIT Madras. For more details on ...

**Body-Effect** <http://www.eneradi.com>.

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**IR Drop & EM - English Version** This video contain IR Drop & EM in English for basic Electronics & **VLSI engineers**.as per my knowledge i shared the details in ...

**LATCH-UP IN CMOS CIRCUITS** Hello Everyone, This videos explains the latch-up phenomenon in **CMOS** Circuits. Subscribe this channel for more videos on ...

**Intel: The Making of a Chip with 22nm/3D Transistors | Intel** This video shows the process of how computer chips are made using Intel's world leading 22nm manufacturing technology with ...

**Simple CMOS** a brief tutorial explanation of **CMOS** for electronics students.

**Top 50 VLSI ece technical interview questions and answers tutorial for Fresher Experienced videos** Top 50 **VLSI** ece technical interview questions and answers tutorial for Fresher Experienced videos **vlsi** interview questions and ...

**How MOSFETs and Field-Effect Transistors Work!** <http://www.bring-knowledge-to-the-world.com/> This video explains to you how MOSFETs (metal-oxide-semiconductor field-effect ...

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**The CMOS Inverter** The DC transfer curve of the **CMOS** inverter is explained. The N-Channel and P-Channel connection and operation is presented.

**CMOS FABRICATION STEPS IN VLSI DESIGN** This topic consist of NMOS, PMOS and TWINTUBE fabrication process in **VLSI** Design.

**CMOS Inverter CMOS** Inverter Watch more videos at <https://www.tutorialspoint.com/videotutorials/index.htm> Lecture By: Ms. Gowthami Swarna, ...

**STICK DIAGRAM - simplified (VLSI)** Topic: STICK DIAGRAM Subject: **VLSI** .....  
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**Why is polysilicon used as a gate contact instead of metal in CMOS ? - English Version**  
This video contain Why is polysilicon used as a gate contact instead of metal in **CMOS** ? for basic Electronics & **VLSI engineers**.as ...

**{ } VLSI } 14 } Latch-up & CMOS Technologies }** This lecture discusses latch-up phenomenon in SMOS circuits. What is latch-up, what are typical methods to mitigate latch-up and ...

## **VLSI Fabrication Process**

**Mod-01 Lec-06 Power Estimation and Control in CMOS VLSI circuits** Advanced **VLSI** Design by Prof. A.N. Chandorkar, Prof. D.K. Sharma, Prof. Sachin Patkar, Prof. Virendra Singh,Department

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